

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listing, of claims in the application:

Listing of Claims:

1. (Currently Amended) A semiconductor device comprising:
a first insulating interlayer on a semiconductor substrate;
a first contact hole formed in the first insulating interlayer;
a first cell plug on the semiconductor substrate through the first insulating interlayer[[:]], wherein the first cell plug is formed in the first contact hole;
a second insulating interlayer on the first insulating interlayer;
a second contact hole formed in the second insulating interlayer;
a silicide contact on a predetermined surface of the first cell plug through the first insulating interlayer; and
a second cell plug on the silicide contact through the second insulating interlayer, wherein the second cell plug contacts the second contact hole.
2. (Original) The semiconductor device of claim 1, wherein the second cell plug is formed of a metallic material with low resistance.

3. (Original) The semiconductor device of claim 2, wherein the metallic material is a titanium nitride film.

4. (Original) The semiconductor device of claim 2, wherein the first cell plug is formed of a silicon material.

5. (Original) The semiconductor device of claim 4, wherein the silicon material is either a monosilicon film or a polysilicon film.

6. (Original) The semiconductor device of claim 1, wherein the silicide contact is formed using a metal film.

7. (Original) The semiconductor of claim 6, wherein the metal film is formed of titanium (Ti).

8. (Original) The semiconductor device of claim 1, wherein the silicide contact is formed directly on the first cell plug through the first insulating interlayer.

9. (Original) The semiconductor device of claim 1, wherein the second cell plug functions as a barrier metal layer.

10. (New) A semiconductor device comprising:

- a first insulating interlayer on a semiconductor substrate;
- a first contact hole formed in the first insulating interlayer;
- a first cell plug on the semiconductor substrate through the first insulating interlayer, wherein the first cell plug is formed in the first contact hole;
- a second insulating interlayer on the first insulating interlayer;
- a second contact hole formed in the second insulating interlayer;
- a silicide contact on a predetermined surface of the first cell plug through the first insulating interlayer; and
- a second cell plug on the silicide contact through the second insulating interlayer, wherein a portion of the second cell plug is formed in the first contact hole.

11. (New) A semiconductor device comprising:

- a first insulating interlayer on a semiconductor substrate;
- a first contact hole formed in the first insulating interlayer;
- a first cell plug on the semiconductor substrate through the first insulating interlayer, wherein the first cell plug is formed in the first contact hole;
- a second insulating interlayer on the first insulating interlayer;
- a second contact hole formed in the second insulating interlayer;

a silicide contact on a predetermined surface of the first cell plug through the first insulating interlayer; and

a second cell plug directly on the silicide contact through the second insulating interlayer.